

# Microprocessor Job Interview Questions And Answers



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## Microprocessor Interview Questions And Answers Guide.

### Question - 1:

Whats the speed and device maximum specs for firewire?

#### Ans:

No Answer is Posted For this Question

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### Question - 2:

What is SIM and RIM instructions?

#### Ans:

SIM is Set Interrupt Mask. Used to mask the hardware interrupts.

RIM is Read Interrupt Mask. Used to check whether the interrupt is Masked or not.

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### Question - 3:

What are the major flags in 8086?

#### Ans:

In 8086 Carry flag, Parity flag, Auxiliary carry flag, Zero flag, Overflow flag, Trace flag, Interrupt flag, Direction flag, and Sign flag.

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### Question - 4:

Which Stack is used in 8086? k is used in 8086?

#### Ans:

FIFO (First In First Out) stack is used in 8086. In this type of Stack the first stored information is retrieved first.

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### Question - 5:

What does the EU do?

#### Ans:

Execution Unit receives program instruction codes and data from BIU, executes these instructions and store the result in general registers.

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### Question - 6:

What are the various segment registers in 8086?

#### Ans:

Code, Data, Stack, Extra Segment registers in 8086.

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### Question - 7:

What are the different functional units in 8086?

#### Ans:

Bus Interface Unit and Execution unit, are the two different functional units in 8086.

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**Question - 8:**

What is Non-Maskable interrupts?

**Ans:**

An interrupt which can be never be turned off (ie. disabled) is known as Non-Maskable interrupt

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**Question - 9:**

What is meant by Maskable interrupts?

**Ans:**

An interrupt that can be turned off by the programmer is known as Maskable interrupt.

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**Question - 10:**

What is the Maximum clock frequency in 8086?

**Ans:**

5 Mhz is the Maximum clock frequency in 8086.

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**Question - 11:**

Have you studied buses? What types?

**Ans:**

There are three types of buses.

Address bus: This is used to carry the Address to the memory to fetch either Instruction or Data.

Data bus : This is used to carry the Data from the memory.

Control bus : This is used to carry the Control signals like RD/WR, Select etc.

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**Question - 12:**

How many bit combinations are there in a byte?

**Ans:**

Byte contains 8 combinations of bits.

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**Question - 13:**

Where does the CPU Enhanced mode originate from?

**Ans:**

Intel's 80386 was the first 32-bit processor, and since the company had to backward-support the 8086. All the modern Intel-based processors run in the Enhanced mode, capable of switching between Real mode (just like the real 8086) and Protected mode, which is the current mode of operation.

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**Question - 14:**

What's the speed and device maximum specs for Firewire?

**Ans:**

IEEE 1394 (Firewire) supports the maximum of 63 connected devices with speeds up to 400 Mbps. Where's MBR located on the disk? Main Boot Record is located in sector 0, track 0, head 0, cylinder 0 of the primary active partition.

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**Question - 15:**

Name the processor lines of two major manufacturer?

**Ans:**

High-end: Intel - Pentium (II, III, 4), AMD - Athlon. Low-end: Intel - Celeron, AMD - Duron. 64-bit: Intel - Itanium 2, AMD - Opteron.

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**Question - 16:**

What is 1st / 2nd / 3rd / 4th generation processor?

**Ans:**

The processor made of PMOS / NMOS / HMOS / HCMOS technology is called 1st / 2nd / 3rd / 4th generation processor, and it is made up of 4 / 8 / 16 / 32 bits.

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**Question - 17:**

What are the various registers in 8085?



**Ans:**

Accumulator register, Temporary register, Instruction register, Stack Pointer, Program Counter are the various registers in 8085

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**Question - 18:**

What is Program counter?

**Ans:**

Program counter holds the address of either the first byte of the next instruction to be fetched for execution or the address of the next byte of a multi byte instruction, which has not been completely fetched. In both the cases it gets incremented automatically one by one as the instruction bytes get fetched. Also Program register keeps the address of the next instruction

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**Question - 19:**

Which Stack is used in 8085?

**Ans:**

LIFO (Last In First Out) stack is used in 8085. In this type of Stack the last stored information can be retrieved first

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**Question - 20:**

What happens when HLT instruction is executed in processor?

**Ans:**

The Micro Processor enters into Halt-State and the buses are tri-stated.

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**Question - 21:**

What is Tri-state logic?

**Ans:**

Three Logic Levels are used and they are High, Low, High impedance state. The high and low are normal logic levels & high impedance state is electrical open circuit conditions. Tri-state logic has a third line called enable line.

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**Question - 22:**

In 8085 which is called as High order / Low order Register?

**Ans:**

Flag is called as Low order register & Accumulator is called as High order Register.

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**Question - 23:**

Why crystal is a preferred clock source?

**Ans:**

Because of high stability, large Q (Quality Factor) & the frequency that doesn't drift with aging. Crystal is used as a clock source most of the times.

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**Question - 24:**

What are the flags in 8086?

**Ans:**

In 8086 Carry flag, Parity flag, Auxiliary carry flag, Zero flag, Overflow flag, Trace flag, Interrupt flag, Direction flag, and Sign flag.

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**Question - 25:**

What is a Microprocessor?

**Ans:**

Microprocessor is a program-controlled device, which fetches the instructions from memory, decodes and executes the instructions. Most Micro Processor are single-chip devices.

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