

DSP Job Interview Questions And Answers



Interview Questions Answers

<https://interviewquestionsanswers.org/>

About Interview Questions Answers

Interview Questions Answers . ORG is an interview preparation guide of thousands of Job Interview Questions And Answers, Job Interviews are always stressful even for job seekers who have gone on countless interviews. The best way to reduce the stress is to be prepared for your job interview. Take the time to review the standard interview questions you will most likely be asked. These interview questions and answers on DSP will help you strengthen your technical skills, prepare for the interviews and quickly revise the concepts.

If you find any **question or answer** is incorrect or incomplete then you can **submit your question or answer** directly with out any registration or login at our website. You just need to visit [DSP Interview Questions And Answers](#) to add your answer click on the *Submit Your Answer* links on the website; with each question to post your answer, if you want to ask any question then you will have a link *Submit Your Question*; that's will add your question in DSP category. To ensure quality, each submission is checked by our team, before it becomes live. This [DSP Interview preparation PDF](#) was generated at **Wednesday 29th November, 2023**

You can follow us on FaceBook for latest Jobs, Updates and other interviews material.
www.facebook.com/InterviewQuestionsAnswers.Org

Follow us on Twitter for latest Jobs and interview preparation guides.
<https://twitter.com/InterviewQA>

If you need any further assistance or have queries regarding this document or its material or any of other inquiry, please do not hesitate to contact us.

Best Of Luck.

Interview Questions Answers.ORG Team
<https://InterviewQuestionsAnswers.ORG/Support@InterviewQuestionsAnswers.ORG>



DSP Interview Questions And Answers Guide.

Question - 1:

Explain Is the Gibbs phenomenon ever a factor?

Ans:

Yes Gibbs phenomenon becomes constraining when we are analysing signals containing frequency tones quite close to each other. If the side lobes of the windowing function are significant then it leads to energy leakages between the frequency bins/sub-bands. Thus very close lying frequency tones get their magnitudes smeared up in the process.

[View All Answers](#)

Question - 2:

What is an anti aliasing filter and why is it required?

Ans:

Anti aliasing filter reduces errors due to aliasing. If a signal is sampled at 8 kS/S, the max frequency of the input should be 4 kHz. Otherwise, aliasing errors will result. Typically a 3.4kHz will have an image of 4.6 kHz, and one uses a sharp cut off filter with gain of about 1 at 3.4kHz and gain of about 0.01 at 4.6 kHz to effectively guard against aliasing. Thus one does not quite choose max frequency as simply $f_s/2$ where f_s is sampling frequency. One has to have a guard band of about 10% of this f_{max} , and chooses max signal frequency as $0.9*f_s/2$

[View All Answers](#)

Question - 3:

How do we implement a fourth order Butterworth LP filter at 1kHz if sampling frequency is 8 kHz?

Ans:

A fourth order Butterworth filter can be made as cascade of two second order LP filters with zeta of 0.924 and 0.383. One can use a bilinear transformation approach for realising second order LP filters. Using this technique described well in many texts, one can make two second order LP filters and cascade them.

[View All Answers](#)

Question - 4:

Suppose we are sending address of the slave and then data then after I want to read the data which I was sent recently, in that case before my reading is there any need to send a stop bit before read?

Ans:

Before reading the data if you are giving the stop bit then the communication is stopped. So after sending the data you will give the stop bit.

[View All Answers](#)

Question - 5:

Explain what is Dirac delta function and its Fourier transform and its importance?

Ans:



Dirac delta is a continuous time function with unit area and infinite amplitude at $t=0$.

the fourier transform of dirac delta is 1.

using dirac delta as an input to the system, we can get the system response. it is used to study the behavior of the circuit.

we can use this system behavior to find the output for any input.

[View All Answers](#)

Question - 6:

Can we create a table with out primary key?

Ans:

yes we can create

```
CREATE TABLE Orders
```

```
(
```

```
    OrderID SMALLINT UNSIGNED NOT NULL PRIMARY KEY,
```

```
    ModelID SMALLINT UNSIGNED NOT NULL,
```

```
    ModelDescrip
```

```
);
```

[View All Answers](#)

Question - 7:

What is the difference between ProtoPlus and ProtoPlus Lite?

Ans:

ProtoPlus prototyping daughter card - A plug-in, 2-connector, multi-layer, low noise, and stackable prototyping board that plugs into the Texas Instruments DSK and EVM DSP development systems.

ProtoPlus Lite prototyping daughter card - A Low cost, 2-connector, plug-in prototyping board that plugs into the Texas Instruments DSK and EVM DSP development systems.

[View All Answers](#)

Question - 8:

Do you know How is the non-periodic nature of the input signal handled?

Ans:

Fourier series is applied for periodic signals since they violate Dirichlet's conditions. This will give the fundamental and harmonic signal components for periodic signals.

For non-periodic signals if we need frequency analysis as a whole then fourier transform is applied for the entire duration. Provided its energy is finite and follows other conditions as laid out by Dirichlet.

[View All Answers](#)

Question - 9:

Please write a code in C / Verilog to implement a basic FIR filter?

Ans:

```
%program for FIR filters
```

```
disp('choose the window from the list');
```

```
ch=menu('types of windows','bartlett','blackman','hamming','hanning','kaiser','rectangular');
```

```
rp=input('enter the passband ripple in db');
```

```
rs=input('enter the stopband ripple in db');
```

```
wsample=input('enter sampling frequency in hertz');
```

```
wp=input('enter the passband frequency in hertz');
```

```
ws=input('enter the stopband frequency in hertz');
```

```
wp=2*wp/wsample; ws=2*ws/wsample;
```

```
p=20*log10(sqrt(rp*rs))-13;
```

```
q=14.6*(ws-wp)/wsample;
```

```
N=1+floor(p/q);
```

```
N1=N;
```

```
if(rem(N,2)==0)
```

```
    N1=N+1;
```

```
else
```

```
    N=N-1;
```

```
end
```

```
switch ch
```

```
case 1
```

```
    y=bartlett(N1);
```



```
case 2
    y=blackman(N1);
case 3
    y=hamming(N1);
case 4
    y=hanning(N1);
case 5
    beta=input('enter beta for kaiser window');
    y=kaiser(N1,beta);
case 6
    y=boxcar(N1);
otherwise
    disp('enter proper window number');
end
disp('select the type of filter from the list');
type=menu('types of
filters','lowpass','highpass','bandpass','bandstop');
switch type
case 1
    b=fir1(N,wp,'low',y);
case 2
    b=fir1(N,wp,'high',y);
case 3
    b=fir1(N,[wp ws],'bandpass',y);
case 4
    b=fir1(N,[wp ws],'stop',y);
otherwise
    disp('enter type number properly');
end
[h,w]=freqz(b,1,512);
magn=20*log10(abs(h));
phase=(180/pi)*unwrap(angle(h));
w=(w*wsample)/(2*pi);
subplot(2,1,1); plot(w,magn),grid on;title('magnitude
plot'); subplot(2,1,2); plot(w,phase),grid on;title('phase
plot');
```

[View All Answers](#)

Computer Embedded Systems Most Popular & Related Interview Guides

- 1 : [Embedded System Interview Questions and Answers.](#)
- 2 : [Very-large-scale integration \(VLSI\) Interview Questions and Answers.](#)
- 3 : [86 Family Interview Questions and Answers.](#)

Follow us on FaceBook

www.facebook.com/InterviewQuestionsAnswers.Org

Follow us on Twitter

<https://twitter.com/InterviewQA>

For any inquiry please do not hesitate to contact us.

Interview Questions Answers.ORG Team

[https://InterviewQuestionsAnswers.ORG/
support@InterviewQuestionsAnswers.ORG](https://InterviewQuestionsAnswers.ORG/support@InterviewQuestionsAnswers.ORG)